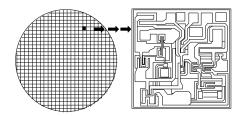
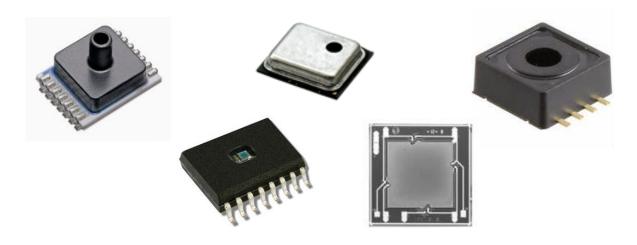
# AEC - Q103 - 002 Rev-<u>A</u> September 17, 2023



# FAILURE MECHANISM BASED STRESS TEST QUALIFICATION FOR MICRO ELECTRO-MECHANICAL SYSTEM (MEMS) PRESSURE SENSOR DEVICES



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# AEC - Q103 - 002 Rev-A September 17, 2023

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# FAILURE MECHANISM BASED STRESS TEST QUALIFICATION FOR MICRO ELECTRO-MECHANICAL SYSTEM (MEMS) PRESSURE SENSOR DEVICES

#### 1. SCOPE

This document contains a set of failure mechanism based stress tests specific to the Micro Electro-Mechanical System (MEMS) Pressure Sensor technologies listed in Section 1.1.1 below. This document shall be used in conjunction with AEC-Q100. The circuit elements of MEMS devices are susceptible to the same failure mechanisms as standard IC's, thus must meet the requirements defined in AEC-Q100. The MEMS portion of these devices, including circuit and package interactions, must meet the requirements defined herein.

The objective is to precipitate failures in an accelerated manner compared to use conditions, or to simulate extreme events to draw out design or intrinsic process deficiencies. This set of tests should not be used indiscriminately. Each qualification project should be examined for:

- a. Any potential new and unique failure mechanisms.
- b. Any situation where these tests/conditions may induce failures that would not be seen in the application.
- c. Any extreme use condition and/or application that could adversely reduce the acceleration.

Use of this document does not relieve the MEMS supplier of their responsibility to meet their own company's internal qualification program. In this document "user" is defined as all customers using a device qualified per this specification. User specific requirements will need to be considered in addition to this specification. The user is responsible to confirm and validate all qualification data that substantiates conformance to this document. Supplier usage of the device temperature grades as stated in this specification in their part information is strongly encouraged.

#### 1.1 Purpose

The purpose of this specification is to determine that a MEMS pressure sensor device is capable of passing the specified stress tests and thus can be expected to give a certain level of quality/reliability in the application.

#### 1.1.1 MEMS Pressure Sensor Technologies

The MEMS Pressure Sensor device technologies considered during the development of this document include:

- Polysilicon surface micro-machined
- Single crystal silicon Deep Reactive Ion Etching (DRIE)
- Bulk micro-machined
- Capping processes including:
  - Glass frit
  - Eutectic bonding
  - Fusion bonding
  - Anodic bonding

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#### 1.1.2 MEMS Pressure Sensor Types and Packaging

MEMS pressure sensor device types included in the scope of this document are as follows:

- A pressure sensing element integrated into a signal conditioning IC ("co-integrated") mounted in an open cavity (gel covered or gel free) package
- A stacked die/side-by-side configuration where a pressure sensing element is mounted on/next to a signal conditioning IC in open cavity (gel covered or gel free) package
- A pressure sensing element mounted into a pre-mold cavity (gel covered or gel free) after overmolding of the signal conditioning IC
- A pressure sensing element mounted into a pre-mold cavity (gel covered or gel free) after package molding
- A pure pressure sensing element consisting of an unpackaged silicon micro-machined piezoresistive pressure sensing element (i.e., bare die delivery)

MEMS pressure sensor packaging includes, but is not limited to, the following:

- Non-Hermetic Cavity Package
- Non-Hermetic Leadframe Cavity Package
- Overmolded Leadframe Package
- Overmolded Laminate Package

#### 1.2 Reference Documents

The current revision of the referenced documents will be in effect at the date of agreement to the qualification plan. Subsequent qualification plans will automatically use updated revisions of these referenced documents.

#### 1.2.1 Automotive

AEC-Q100 Failure Mechanism Based	Stress Test Qualification for Integrated Circuits
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#### 1.2.2 Military

MIL-STD-202	Test Method Standard: Electronic and Electrical Component
MIL-STD-883	Test Method Standard: Microcircuits

#### 1.2.3 Industrial

JEDEC JESD22	Reliability Test Methods for Packaged Devices
DIN 50018	Testing in a saturated atmosphere in the presence of sulphur dioxide
EN 60068-2-60	Environmental testing - Flowing mixed gas corrosion test
ISO 16750-5	Road vehicles - Environmental conditions and testing for electrical and
	electronic equipment – Part 5: Chemical loads

#### 1.3 Definitions

#### 1.3.1 AEC Q103-002 Qualification

Successful completion and documentation of the test results from requirements outlined in this document and AEC-Q100 document allows the supplier to claim that the part is "AEC-Q103-002 qualified".

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#### 1.3.2 AEC Certification

Note that there are no "certifications" for AEC-Q103-002 qualification and there is no certification board run by AEC to qualify parts. Each supplier performs their qualification to AEC standards, considers customer requirements and submits the data to the customer to verify compliance to AEC-Q103-002.

#### 1.3.3 Definition of MEMS Pressure Sensor Part Operating Temperature Grades:

The part operating temperature grades are defined in Table 1 of AEC-Q100. Additional temperature grades applicable to MEMS Pressure Sensor devices are defined in Table 1A below:

Table 1A: Additional MEMS Pressure Sensor Part Operating Temperature Grades

Grade	Ambient Operating Temperature Range
0A	-40°C to +165°C
0B	-40°C to +175°C

All automotive grades as defined in AEC-Q100 apply; the above grades are only needed if ambient operating temperature range exceeds AEC-Q100 grade zero requirements. For all biased tests from Table 2 of this document and Table 2 of AEC-Q100, the junction temperature of the MEMS pressure sensor device during stressing should be equal to or greater than the ambient hot temperature for that grade.

If the minimum <u>and/</u>or maximum ambient temperature as specified in the supplier datasheet cannot be found in Table 1A of this document or Table 1 of AEC-Q100, then the next more challenging part operating temperature grade must be selected. Exceptions include the following:

- If the hot temperature of a chosen part operating temperature grade exceeds the allowed maximum temperature specified in the supplier datasheet, then testing should be limited to the maximum datasheet value. This applies only to biased tests from Table 2 of this document (e.g., PrHTOL) and biased tests from Table 2 of AEC-Q100 (e.g., HTOL, ELFR, PTC). Actual tests and maximum ambient temperature used shall be per mutual agreement between user and supplier.
- Endpoint hot temperature for Pre- and Post-Stress Function/Parameter Verification testing should be limited to the maximum ambient operating temperature specified in the supplier datasheet.

#### 1.3.4 Definition of MEMS Pressure Sensor Part Mechanical Grade:

The part mechanical grades for MEMS pressure sensors are defined in Table 1B below:

Table 1B: MEMS Pressure Sensor Part Mechanical Grades

Grade	Application Requirement
M1	Pressure Sensor – General
M2	Tire Pressure Monitoring System (TPMS) – Rim Mounted

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#### 2. GENERAL REQUIREMENTS

MEMS Pressure Sensor device qualification shall be compliant to AEC-Q100 with additional requirements as defined herein.

#### 2.1 Precedence of Requirements

In the event of conflict in the requirements of this standard and those of any other documents, the following order of precedence applies:

- a. The purchase order (or master purchase agreement terms and conditions)
- b. The (mutually agreed) individual device specification
- c. This document
- d. AEC-Q100
- e. The reference documents in Section 1.2 of this document
- f. The supplier's datasheet

For the device to be considered a qualified part per this specification, the purchase order and/or the individual device specification cannot waive or detract from the requirements of this document.

#### 2.5 Definition of Test Failure After Stressing

In addition to AEC-Q100 requirements, Test Group PS shall be used to disposition rejects from AEC-Q100 temperature cycling or accelerated moisture stresses that are not accelerated failure mechanisms.

#### 3. QUALIFICATION AND REQUALIFICATION

#### 3.1 Qualification of a New MEMS Pressure Sensor Device

Test Group PS provides guidance on stress tests specific to the MEMS element or MEMS to package interactions that occur due to the physical overstress inherent in accelerated temperature cycling and moisture tests at permanent or cycled pressure impact. This test group does not apply to the accelerated failure mechanisms for which the AEC-Q100 stress conditions are derived.

#### 3.2 Criteria for Passing Qualification and Requalification

All failures shall be analyzed for root cause. Only when corrective <u>and preventative</u> actions are in place, have been proven effective <u>for valid failures</u>, and the 8D methodology (Eight Discipline, see JESD671) <u>has been completed</u>, the MEMS pressure sensor device may then be considered AEC-Q103-002 qualified. The burden of proof is on the supplier to convince any subsequent users of that part that is responsible for proving the effectiveness of its 8D is effective.

#### 4. QUALIFICATION TESTS

#### 4.1 General Tests

In addition to well-known IC failure mechanisms, MEMS pressure sensor devices require specific qualification tests to verify performance of both the MEMS die and the packaging in an application environment taking into account mutual interactions, including environmental and functional loading. Unique qualification tests and/or test sequences are defined for MEMS pressure sensor devices <u>due to</u> the presence of a pressure port and exposure of the pressure membrane to environmental influences. Stress tests have been defined on the basis of interactions of environmental and functional loads of MEMS pressure sensor devices (see Figure 1).

- a. Environmental loads include pressure, temperature, and humidity.
- b. Functional loads include mechanical and chemical.

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- c. The set of loads and diverse interactions of their states (e.g., constant, cycled/pulsed, rapid change, shock) define the unique qualification tests and their sequences:
  - Pressure load states define the pressure life tests, pressure pulsed tests, and proof/burst tests:
    - o Interaction between pressure, temperature, humidity, and chemical loads defines preconditioning before pressure tests and chemical tests.
    - Interaction between pressure, temperature, and humidity makes HAST and UHST more preferable tests than THB and AC.
  - Chemical load states define the chemical tests such as corrosive atmosphere, chemical resistance, salt immersion, etc.;
    - Interaction between temperature, humidity, chemical, and mechanical loads defines the internal visual inspection and wire bond pull testing performed post-chemical and postmechanical tests.

The stress test requirement flow for qualification of a new MEMS pressure sensor device is shown in Figure 2. This specification defines the requirements for the qualification of MEMS pressure sensor devices. It is to be used in conjunction with AEC-Q100, rather than in lieu of. AEC-Q100 shall be used to qualify the active circuitry and basic package integrity of the MEMS pressure sensor device. Qualification tests and/or test sequences specific to MEMS pressure sensor devices are detailed in Figure 2 and Table 2A. Table 2B lists the AEC-Q100 tests updated to address MEMS pressure sensor device failure mechanisms.

Not all AEC-Q100 tests apply to all MEMS pressure sensor devices. For example, Constant Acceleration (CA, test #G3) as Pre-conditioning before Mechanical Shock (MS, test #G1) and Variable Frequency Vibration (VFV, test #G2) is only applicable to TPMS devices. The applicable tests for the particular device type are indicated in the "Note" column of Tables 2A and 2B. The "Additional Requirements" column of Tables 2A and 2B also serves to highlight test requirements that supersede those described in the referenced test method. Any unique qualification tests or conditions requested by the user and not specified in this document shall be negotiated between the supplier and user requesting the test. The Target Failure Mechanism column serves to provide guidance as to the rationale for the requirement.

#### 4.2 Device Specific Tests

MEMS pressure sensor device specific tests shall be performed in accordance with Section 4.2 of AEC-Q100.

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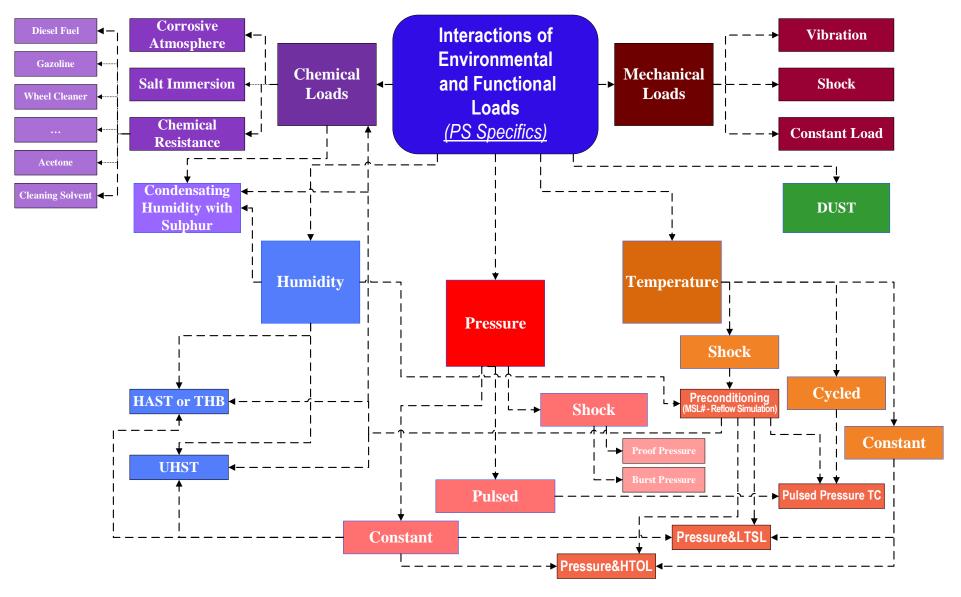


Figure 1: Basis of Determination of Qualification Tests

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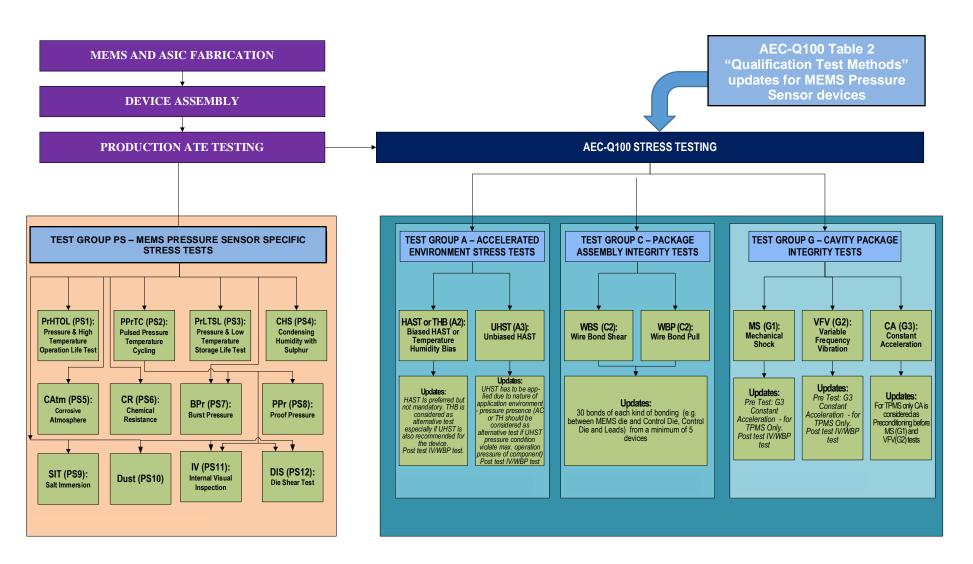


Figure 2: MEMS Pressure Sensor Device Qualification Test Flow

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# Table 2A: MEMS Pressure Sensor Specific Qualification Test Methods

Note: AEC-Q100 shall be used to qualify the active circuitry contained within the MEMS pressure sensor device, as well as package integrity for the active circuitry. The following tests are specific to MEMS pressure sensor technology and package integrity for the MEMS technology. It is to be used in conjunction with AEC-Q100, rather than in lieu of.

	TEST GROUP PS – MEMS PRESSURE SENSOR SPECIFIC STRESS TESTS											
STRESS	ABV	#	NOTES	SAMPLE SIZE (**) /LOT	NUMBER OF LOTS	ACCEPT CRITERIA	TEST METHOD	ADDITIONAL REQUIREMENTS	TARGETED MEMS FAILURE MECHANISM			
Pressure & High Temperature Operating Life Test	PrHTOL	PS1	G	77	3	0 Fails	Customer tailored plus JEDEC JESD22- A108	Pre-Test: Preconditioning (PC) per AEC-Q100 Test #A1.  HTOL per AEC-Q100 Test #B1 requirements taking into account the added MEMS grades:  Grade 0A: +165°C Ta for 1000 hours  Grade 0B: +175°C Ta for 1000 hours  Pressure condition: maximum operation pressure, Pmax(op), according to MEMS device pressure range  TEST before and after PrHTOL at room, cold, and hot temperature (in that order).  Continuous monitoring of pressure sensor output signal is recommended.  (PrHTOL replaces AEC-Q100 Test #B1 HTOL)	Bulk die or diffusion defects, film stability and ionic contamination surface-charge spreading, mechanical creep, membrane fatigue, para- metric stability			
Pulsed Pressure Temperature Cycling	PPrTC	PS2	G	<u>45</u>	3	0 Fails	Customer tailored plus JEDEC JESD22- A104	This test and its conditions is performed per agreement between user and supplier on a case-by-case basis.  Pre-Test: Preconditioning (PC) per AEC-Q100 Test #A1  TC per AEC-Q100 Test #A4 requirements taking into account the added MEMS grades:  Grade 0A: Ta of -40°C to +165°C for 1000 cycles.  Grade 0B: Ta of -40°C to +175°C for 1000 cycles.  Grade 0: Ta of -40°C to +150°C for 1000 cycles.  Grade 1: Ta of -40°C to +125°C for 1000 cycles.  Grades 2 and 3: Ta -40°C to +105°C for 1000 cycles.  Typical frequency <1-2 cycle per hour and typical soak time minimum 5 minutes  Pressure cycling: fp=0.1 Hz in minimum operating pressure,  Pmin(op), and maximum operating pressure, Pmax(op),  pressure range (pressure rise and fall time should correspond to pressure mission profile from data sheet or to be adjusted according to application condition)  TEST before and after PPrTC at cold and hot temperature.  Post-Test: IV (PS11) and WBP (C2) test for 5 devices; DIS (PS12) test for 5 parts; Burst Pressure Test (PS7) and Proof Pressure Test (PS8) for one lot.	Wire bond, wire, die bond, gel aeration, package failures, volumetric gel changes, mechanical creep, membrane fatigue, parametric stability			

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	TEST G	ROUF	PS-	MEMS I	PRESSU	RE SEN	SOR SPEC	IFIC STRESS TESTS (CONTINUED)	
STRESS	ABV	#	NOTES	SAMPLE SIZE (**)/ LOT	NUMBER OF LOTS	ACCEPT CRITERIA	TEST METHOD	ADDITIONAL REQUIREMENTS	TARGETED MEMS FAILURE MECHANISM
								Pre-Test: Preconditioning (PC) per AEC-Q100 Test #A1	
Pressure & Low Temperature <u>Storage</u> Life Test	PrLT <u>S</u> L	PS3	G	77	1	0 Fails	<u>JEDEC</u> <u>JESD22-A119</u>	LTSL for 1000 hours at minimum operating temperature,     Tmin(op)  Pressure condition: maximum operation pressure, Pmax(op),     according to MEMS device pressure range  TEST before and after PrLTSL at room, hot, and cold     temperature (strongly recommended interim readouts at     time intervals for end-points acc. to item 5 of Appendix 4.1,     e.g., after PC, 0 hour, 500 hour, 1000 hour)	Bulk die defects or diffusion defects, mechanical creep, membrane fatigue, parametric stability
Condensing Humidity with Sulphur (can be also testing in a saturated atmosphere in the presence of sulphur dioxide)	снѕ	PS4	G	45	1	0 Fails	DIN 50018	This test and its conditions is performed per agreement between user and supplier on a case-by-case basis, e.g., on the second level assembly***.  Pre-Test: Preconditioning (PC) per AEC-Q100 Test #A1  Bias Cycling condition: Vddmax, 1 hour ON, 1 hour OFF Test Cycle condition: 10 Cycles (1 cycle per 24hrs) according to DIN-50018  Sulphur condition: Concentration of SO2 at the beginning of each test cycle = 0.33 as percentage to volume  TEST before after CHS at room temperature.  Post-Test: IV (PS11) and WBP (C2) test for 5 devices.  * Note: Certain applications may require modified test conditions.	Corrosion, wire bond, wire, contamination, volumetric gel changes, parametric stability
Corrosive Atmosphere	CAtm	PS5	G	10	1	0 Fails	EN 60068-2-60/ Method 4	This test and its conditions is performed per agreement between user and supplier on a case-by-case basis, e.g., on the second level assembly***.  Pre-Test: Preconditioning (PC) per AEC-Q100 Test #A1  Temperature condition: +25°C Humidity condition: 75% Flow Rate: 1m³/h Gases: SO2 at 0.20ppm; H2S at 0.01ppm; NO2 at 0.20ppm; Cl2 at 0.01ppm Duration: 14 days  TEST before and after CAtm at room temperature. Post-Test: IV (PS11) and WBP (C2) test for 5 devices. * Note: Certain applications may require modified test conditions.	Gel swelling, volumetric gel changes, corrosion, wire bond, wire, contamination parametric stability

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	TEST G	ROUF	PS-	MEMS I	PRESSU	RE SEN	SOR SPEC	IFIC STRESS TESTS (CONTINUED)	
STRESS	ABV	#	NOTES	SAMPLE SIZE (**)/ LOT	NUMBER OF LOTS	ACCEPT CRITERIA	TEST METHOD	ADDITIONAL REQUIREMENTS	TARGETED MEMS FAILURE MECHANISM
Chemical Resistance (can be also solvent immersion)	CR	PS6	G	5 per chemical	1	0 Fails	Customer tailored plus ISO 16750-5	, , , , , , , , , , , , , , , , , , , ,	Gel swelling, volumetric gel changes, corrosion, wire bond, wire, contamination, parametric stability
Burst Pressure	BPr	PS7	G	15	3	0 Fails (state passing level in the data sheet)	Customer tailored	Burst Pressure: the maximum pressure that may be applied to the sensor without a catastrophic failure.  Temperature condition: maximum operating temperature, Tmax(op)  Pressure condition: 5 x Pfull-scale = 5 x [Pmax(op)-Pmin(op)]  Duration: 10 minutes, 1 time  For Relative Pressure Sensors, apply pressure from back and front sides (i.e., perform Front-Side Burst Pressure Test and Back-Side Burst Pressure Test). Due to the destructive nature of the test, separate devices must be used for each test.  Device shall be classified according to the maximum withstand pressure level. Devices should be stepped in pressure at 0.5 x Pfull-scale increments. Device levels < 5 x Pfull-scale shall be documented in the supplier datasheet.  TEST before and after BPr at room temperature.  * Note: Certain applications may require modified test conditions.	Diaphragm fracture, adhesive or cohesive failure of die attach

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	TEST G	ROU	PS-	MEMS I	PRESSU	RE SEN	SOR SPEC	IFIC STRESS TESTS (CONTINUED)	
STRESS	ABV	#	NOTES	SAMPLE SIZE (**)/ LOT	NUMBER OF LOTS	ACCEPT CRITERIA	TEST METHOD	ADDITIONAL REQUIREMENTS	TARGETED MEMS FAILURE MECHANISM
Proof Pressure	PPr	PS8	G	15	3	0 Fails (state passing level in the data sheet)	Customer tailored	Proof Pressure: the maximum pressure that may be applied to the sensor without causing a change in performance with respect to the specifications (i.e., pressure that a sensor can routinely see without a permanent change in the output).  Temperature condition: maximum operating temperature, Tmax(op)  Pressure condition: 3 x Pfull-scale = 3 x [Pmax(op)-Pmin(op)]  Duration: 10 minutes, 10 times  For Relative Pressure Sensors, apply pressure from back and front sides (i.e., perform Front-Side Proof Pressure Test and Back-Side Proof Pressure Test).  Device shall be classified according to the maximum withstand pressure level. Devices should be stepped in pressure at 0.5 x Pfull-scale increments. Device levels < 3 x Pfull-scale shall be documented in the supplier datasheet.  TEST before and after PPr at room temperature.  * Note: Certain applications may require modified test conditions.	Diaphragm fracture, adhesive or cohesive failure of die attach
Salt Immersion Test	SIT	PS9	G	15	1	0 Fails	MIL-STD-883 Method 1002	This test and its conditions is performed per agreement between user and supplier on a case-by-case basis, e.g., on the second level assembly***.  Pre-Test: Preconditioning (PC) per AEC-Q100 Test #A1  Test conditions: 5 cycles of immersion between DI water at 65±3°C (60 min. dwell) and saturated salt water at 0±3°C (60 min. dwell) with 10 sec maximum transfer time. Immerse in DI water for 10 sec after the 5 cycles  TEST before and after SIT at room temperature.  Post-Test: IV (PS11) and WBP (C2) test for 5 devices.  * Note: Certain applications may require modified test conditions.	Package failure, corrosion, contamination

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	TEST GROUP PS - MEMS PRESSURE SENSOR SPECIFIC STRESS TESTS (CONTINUED)											
STRESS	ABV	#	NOTES	SAMPLE SIZE (**)/ LOT	NUMBER OF LOTS	ACCEPT CRITERIA	TEST METHOD	ADDITIONAL REQUIREMENTS	TARGETED MEMS FAILURE MECHANISM			
Dust	DST	PS10	G	15	1	0 Fails	MIL-STD-202G Method 110A	This test and its conditions is performed per agreement between user and supplier on a case-by-case basis, e.g., on the second level assembly***.  Test conditions according to mission profile (protection class, if any)  TEST before and after DST at room temperature.  * Note: Certain applications may require modified test conditions.	Dust contamination			
Internal Visual Inspection	IV	PS11	G	5	3	0 Fails	MIL-STD-883 Method 2013	Internal Visual Inspection for virgin parts and post PS2, PS4, PS6, PS8, PS9, A2, A3, G1, and G2 tests.				
Die Shear Test	DIS	PS12	G	5	3	C <sub>PK</sub> >1.67 or 0 Fails after <b>PPrTC</b> (PS2)	MIL-STD-883 Method 2019	MEMS Pressure Sensor Die Shear Test conditions: DIS is not required for wafer bonding. It should be applied to the die of the pressure sensing element integrated with the interface chip, or in case of stacked die or side-by-side die design, applied to the pressure sensing element.  For flexible die attach glue where MIL-STD-883 Method 2019 criteria and accept criteria are not applicable:  Test zero hour (pre-stress and before pre-conditioning and after relevant stress testing for interfaces PPrTC test the same sample size again.  Compare distributions and look at the mean before and after stress. If the value after stress did not change for more than 50% (plus or minus) the test is passed. That means the stress test did not alter the gluing properties of the glue sufficiently to endanger the reliability of the part.				

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# Table 2B: AEC-Q100 Qualification Test Methods Updated for MEMS Pressure Sensor Devices

(AEC-Q100 Table 2 tests updated to address MEMS pressure sensor device failure mechanisms)

		U	PDATE	D TEST G	ROUP A	- ACCELE	RATED ENV	IRONMENT STRESS TESTS	
STRESS	ABV	#	NOTES	SAMPLE SIZE (**)/LOT	NUMBER OF LOTS	ACCEPT CRITERIA	TEST METHOD	ADDITIONAL REQUIREMENTS	TARGETED MEMS FAILURE MECHANISM
Biased HAST or Temperature- Humidity-Bias	HAST or THB	A2		77	3	0 Fails	JEDEC JESD22-A110 or JESD22-A101	Pre-Test: Preconditioning (PC) per AEC-Q100 Test #A1 before HAST (130°C/85%RH for 96 hours, or 110°C/85%RH for 264 hours), or THB (85°C/85%RH for 1000 hours)  TEST before and after HAST (or THB) at room and hot temperature.  HAST is preferred but not mandatory. THB is considered an alternate test, especially if UHST is also performed for the device.  Post-Test: IV (PS11) and WBP (C2) test for 5 devices.	Shift from ionic effect, moisture ingress, wire bond, package failure, gel swelling, parametric stability.
Unbiased HAST or Autoclave or Temperature- Humidity without Bias	UHST or AC or TH	АЗ	G	77	3	0 Fails	JEDEC JESD22-A118 or JESD22-A102 or JESD22- <u>A101</u>	Pre-Test: Preconditioning (PC) per AEC-Q100 Test #A1 before unbiased HAST (130°C/85%RH for 96 hours, or 110°C/85%RH for 264 hours) or the special conditions of AC (121°C/15psig for 96 hours) or TH (85°C/85%RH for 1000 hours).  TEST before and after UHST (or AC or TH) at room temperature.  Unbiased HAST shall be applied for MEMS pressure sensor devices due to nature of the application environment (i.e., pressure presence). AC should be considered an alternate test if HAST pressure conditions violate the device maximum operation pressure. TH should be considered an alternate test for packages sensitive to high temperatures and pressure.  Post-Test: IV (PS11) and WBP (C2) test for 5 devices.	Wire bond, package failure, gel swelling, parametric stability.
			UPDA	TED TES	T GROUF	C - PAC	KAGE ASSEI	MBLY INTEGRITY TESTS	
Wire Bond Shear	WBS	C1	G	of bond	30 bonds of each kind of bonding (e.g., between MEMS die and control die, control die and leads) from a minimum of 5 devices		AEC Q100-001 AEC Q003	See additional requirements for test C1 and C2 in Table 2 of AEC-Q100.  Perform WBS test for virgin devices.  Perform WBP test for virgin devices and post PS2, PS4, PS5, PS6, PS9, A2, A3, G1, and G2 tests.	
Wire Bond Pull	WBP	C2	G	control die			MIL-STD-883 Method 2011 AEC Q003		

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# Table 2B: AEC-Q100 Qualification Test Methods Updated for MEMS Pressure Sensor Devices (continued)

(AEC-Q100 Table 2 tests updated to address MEMS pressure sensor device failure mechanisms)

	UPDATED TEST GROUP G – CAVITY PACKAGE INTEGRITY TESTS											
STRESS	ABV	#	NOTES	SAMPLE SIZE (**)/LOT	NUMBER OF LOTS	ACCEPT CRITERIA	TEST METHOD	ADDITIONAL REQUIREMENTS	TARGETED MEMS FAILURE MECHANISM			
Mechanical Shock	MS	G1	G	39	3	0 Fails	JEDEC JESD22-B110	Grade M1:  Test conditions: 5 pulses in both directions of each axis, 0.3 ms duration, 6000 g peak acceleration  Grade M2:  Pre-Test: Constant Acceleration (CA) per Test #G3 below  Test conditions: 10 pulses in both directions of each axis, 0.3 ms duration, 6000 g peak acceleration  Alternate Test condition: according to mission profile (mechanical conditions defined by mounting location)  TEST before and after MS at room temperature.  Post-Test: IV (PS11) and WBP (C2) test for 5 devices.				
Variable Frequency Vibration	VFV	G2	G	39	3	0 Fails	JEDEC JESD22-B103	Grade M1:  Test conditions: Per AEC-Q100 (50 g, 20Hz to 2kHz), stress shall be applied to each of three mutually perpendicular axes in plus and minus directions Grade M2:  Pre-Test: Constant Acceleration (CA) per Test #G3 below  Test conditions: Per AEC-Q100 (50 g, 10Hz to 2kHz in 1 hour), stress shall be applied to each of three mutually perpendicular axes in plus and minus directions Alternate Test condition: according to mission profile (mechanical conditions defined by mounting location)  TEST before and after VFV at room temperature. Post-Test: IV (PS11) and WBP (C2) test for 5 devices.	Diaphragm fracture, package failure, die and wire bonds.			
Constant Acceleration	CA	G3	G	39 (78 for TPMS only)	3	0 Fails	MIL-STD-883 Method 2001	Grade M1:  • Test conditions: Per AEC-Q100 (2000 g for 1 min), stress shall be applied to each of three mutually perpendicular axes in plus and minus directions  • Perform Post-Tests: IV (PS11) and WBP (C2) test for 5 devices  Grade M2:  • Test conditions: Per AEC-Q100 (2500 g for 1 hour), stress shall be applied to each of three mutually perpendicular axes in plus and minus directions  Alternate Test condition: according to mission profile (mechanical conditions defined by mounting location)  TEST before and after CA at room temperature.				

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#### Legend for Tables 2A and 2B

#### Notes:

- \*\* Sample size per life tests at bare die delivery. In case of bare die delivery (e.g., piezo-resistive pressure sensing element), test samples must be mounted on a "test substrate" or in ceramic packaging. Optional recommendation is joint qualification where user sub processes are implemented with reduced sample sizes per agreement between user and supplier.
- **G** Generic data allowed. See AEC-Q100, Section 2.3 and Appendix 1 of this document.
- # Reference Number for the particular test.
- \*\*\* 2nd Level Assembly Definition: this is the housing, which builds the interface to the customer with bushings or connectors (typical assembly technologies are soldering, press fit or laser welding)

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#### Table 3: Process Change Qualification Guidelines for the Selection of Tests for MEMS Pressure Sensor Devices

- Temperature Humidity Bias / HAST C4 Physical Dimensions Autoclave / Unbiased HAST
- **Temperature Cycling Power Temperature Cycling**
- High Temperature Storage Life High Temperature Operating Life
- Early Life Failure Rate
  NVM Endurance. Data Retention **B3**
- Wire Bond Shear
- Wire Bond Pull Solderability
- C5 C6 Solder Ball Shear
- Lead Integrity Bump Shear Die Pull / Peeling Lid Pull
- Electromigration D2 Time Dependent Dielectric Breakdown
- **Hot Carrier Injection**
- **Negative Bias Temperature** Instability Stress Migration Human Body Model ESD
- **Charged Device Model ESD** Latch-up Electrical Distribution
- E7 Characterization Electromagnetic Compatibility **Short Circuit Characterization** Soft Error Rate E11
- G1-G4 Mechanical Series G5 Package Drop G6 Lid Torque
- G7 Die Shear Internal Water Vapor Pressure & High Temperature Operating Life
  - Pulsed Pressure Temperature Cycling
- Pressure & Low Temperature Storage
- Condensing Humidity with Sulphur PS4
- PS5 Corrosive Atmosphere PS6 Chemical Resistance
- **Burst Pressure** PS8 **Proof Pressure** PS9 Salt Immersion
- **PS11** Internal Visual Inspection

PS12 Die Shear

Note: A letter or "•" indicates that performance of that stress test should be considered for the appropriate process change. Reason for not performing a considered test should be given in the qualification plan or results.

Test Abbreviation					Т	Т							Т		Т	Т	Т			T		Т	T	П		Т	Т	Т	I		Т		-			T		Т		T	Т			1	$\overline{}$			$\neg$
DESION  AEC-Q1109  AEC	Table 2 Test #	<b>A</b> 2	A3	4	45	2	9 Z	<u>8</u>	B2	B3	ઇ	8	3 3	<u> </u>	9	3 6	å	3 8	3 5	3	3 2	3 2	\$ 2	2 2	1 6	3 4	t 1	3 6	ا د	1 2	E11	E12	G1-G4	G5	99 1	5 6	88	2 6	727	PS4	PS5	PS6	PS7	PS8	PS9	PS10	PS11	PS12
DESION  AEC-Q1109  AEC	Test Abbreviation	HAST	JHST	2	)TC	ا د	TSL TSI	10	ELFR.	EDR	NBS	WBP	2 :	SBS	. _	3ST	Į	<u> </u>	- - -	aud	2 2	E	191	MS		- CM	<u>.</u>	A P	AAL S		SER	щ	ИЕСН	OROP	-	2	W	Trac	2112	15 E	CAtm	S.	3Pr	. J.	SIT	ST	۸	SIC
Circuit Renaming   AEC-Q1109	DESIGN																																															٦
Marter primersion/Thickness (michading Pressure Sersing Element) Design Change	Active Element Design																																				•	•	•						П			
Fig.	Circuit Rerouting															A	λEC	C-Q	100																			T							T			
Element   Design Change   WAFER FAB   Ulthography	Wafer Dimension/Thickness (including Pressure Sensor Membrane)																																•				•	E	EE				Е	Е			•	•
Lithography   Die Shrink   Di	MEMS (Pressure Sensing Element) Design Change	•																										•	•				•				•	•	•				•	•				
Die Shrink    Part   Pa	WAFER FAB																																															
Diffusion/Doping   Polysistion   Passivation/Oxide/Interfevel Dielectric   Passivation   Passivati	Lithography		_	_					_	_	_		_					_				_	_			_		_		_							•	•										
Polysilicon   Metalization/Vas/Contacts   Passivation/Oxide/Interfevel   Passivation/Oxide/	Die Shrink																																				•	•										٦
Metalization/Via/Contacts  AEC-Q100  Metalization/Via/Contacts Passivation/Oxide/Interfevel Diselectric  Backside Operation  MEMS (Pressure Sersing Elements) Specific Process  MEMS (Pressure Sersing Elements) S	Diffusion/Doping																																				•	•		İ								٦
Passivation/Oxide/Interlevel Dialectric Backside Operation FAB Site Transfer MRMS (Pressure Sansing Element) Specific Process  WAFER BUMPING Redistribution Layer Under Bump Material Bump Material Bump Site Transfer  AEC-Q100	Polysilicon																																				•	•	•									
Passivation/Oxide/Interlevel Dialectric Backside Operation FAB Site Transfer MRMS (Pressure Sansing Element) Specific Process  WAFER BUMPING Redistribution Layer Under Bump Material Bump Material Bump Site Transfer  AEC-Q100	Metallization/Via/Contacts																Al	EC-	Q10	2																	•	•	•	1	T	T	T		T	П		ᅦ
FAB Site Transfer    MEMS (Pressure Sensing	Passivation/Oxide/Interlevel Dielectric																			-																	•	•	•	К	К	К			к	к	к	
MEMS (Pressure Sensing Element) Specific Process	Backside Operation																																				•	•	•									
Section   Specific Process   Waffer BUMPING	FAB Site Transfer																																				•	•		•	•	•	•	•	•	•	•	•
Martinary   Mart	MEMS (Pressure Sensing Element) Specific Process	•																									•	•					•				•	•	•				•	•				
Redistribution Layer Under Bump Metal  Bump Material Bump Site Transfer  ASSEMBLY  Die Overcoart Underfill Leadframe Plating Bump Material/Metal System Leadframe Material Leadframe Dimension  Wire Bonding Die Schbe/Separate Die Preparation/Clean Package Marking Die Attach Molding Compound Molding Process Hermetic Sealing New Package Substrate/Interposer			_	-						<u> </u>																																						目
Sump Material   Bump Site Transfer   Sump Material   Sump Site Transfer   Sump Material   S	Redistribution Layer																																					•	•									
Bump Naterial   Bump Site Transfer	Under Bump Metal																																					•	•									
ASSEMBLY  Die Overcoat/ Underfill  Leadframe Plating  Bump Material/Metal System  Leadframe Dimension  Wire Bonding  Die Scribe/Separate  Die Preparation/Clean  Package Marking  Die Attach  Molding Compound  Molding Process  Hermetic Sealing  New Package  Substrate/Interposer	Bump Material																	<u>A</u>	EC-C	210	<u> </u>																	•	•									
Die Overcoat/ Underfill Leadframe Plating  Bump Material/Metal System Leadframe Material Leadframe Dimension  Wire Bonding  Die Scribe/Separate  Die Preparation/Clean  Package Marking  Die Attach  Molding Process  Hermetic Sealing  New Package  Substrate/Interposer	Bump Site Transfer																																					•	•									
Leadframe Plating       □	ASSEMBLY																																				•	•	•		•	•	•					
Bump Material/Metal System Leadframe Material Leadframe Material Leadframe Dimension  Wire Bonding Die Scribe/Separate Die Preparation/Clean Package Marking Die Attach Molding Compound Molding Process Hermetic Sealing New Package Substrate/Interposer	Die Overcoat/ Underfill																																				•	•	•	•	•	•	•	•	•	•	•	•
Leadframe Material  Leadframe Dimension  Wire Bonding  Die Scribe/Separate  Die Preparation/Clean  Package Marking  Die Attach  Molding Compound  Molding Process  Hermetic Sealing  New Package  Substrate/Interposer	Leadframe Plating																																				ı	T		•	•	•						•
Leadframe Dimension       ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■	Bump Material/Metal System																																				•	•	•	•	•	•						
Wire Bonding         ■         <	Leadframe Material																																					•	•	•	•	•						•
Die Scribe/Separate  Die Preparation/Clean  Package Marking  Die Attach  Molding Compound  Molding Process  Hermetic Sealing  New Package  Substrate/Interposer	Leadframe Dimension																																					•	•	•	•	•						•
Die Scribe/Separate  Die Preparation/Clean  Package Marking  Die Attach  Molding Compound  Molding Process  Hermetic Sealing  New Package  Substrate/Interposer	Wire Bonding	•	1																																			•	•	•	•	•			•			•
Package Marking  Die Attach  Molding Compound  Molding Process  Hermetic Sealing  New Package  Substrate/Interposer	Die Scribe/Separate																																					•	•									•
Die Attach  Molding Compound  Molding Process  Hermetic Sealing  New Package  Substrate/Interposer	Die Preparation/Clean																	AE	C-Q1	00																	ı	•	•									•
Molding Compound  Molding Process  Hermetic Sealing  New Package  Substrate/Interposer	Package Marking																																				ı											
Molding Process Hermetic Sealing New Package Substrate/Interposer	Die Attach																																				•	•	•	•	•	•	•	•				•
Molding Process Hermetic Sealing New Package Substrate/Interposer	Molding Compound																																				•	•	•	1	•	•	L					•
New Package           Substrate/Interposer	Molding Process																																				•	•	•	1	•	•	T	1	T	П		•
Substrate/Interposer   • • •   •   •   •   •   •   •   •   •	Hermetic Sealing																																				ı	T	T	1	T	T	T		T	П		ᅦ
Substrate/Interposer   • • •   •   •   •   •   •   •   •   •	New Package																																				•	•	•	•	•	•	•	•	•	•	•	•
	Substrate/Interposer																																				•	•	•		T	T	T	T				
	Assembly Site Transfer																																				•	•	•	•	•	•	•	•	•	•	•	•

- Only for peripheral routing
- For symbol rework, new cure time, temp
- If bond to leadfinger Design rule change

- Thickness only
  - MEMS element only Only from non-100% burned-in parts
- Hermetic only
- EPROM or E<sup>2</sup>PROM
- Passivation only For Pb-free devices only
- For devices requiring PTC
- Passivation and gate oxide
- Wire diameter decrease For Solder Ball SMD only

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#### Appendix 1: Definition of a MEMS Pressure Sensor Product Qualification Family

MEMS Pressure Sensor product qualification family shall be compliant to Appendix 1 of AEC-Q100 with additional requirements specific to MEMS Pressure Sensor devices as defined below:

#### A1.1 Product

- a. Specified MEMS Operating Pressure Range
- Specified MEMS Operating Mechanical Condition (e.g., general Pressure Sensor, rim or tire mounted TPMS)
- c. Specified MEMS Operating Environmental Condition (e.g., details of expected harsh operating environment)

#### A1.2 Fab Process

#### a. Wafer Fab Process

- MEMS structure and material
- MEMS silicon cap bonding process and bonding materials
- MEMS internal atmosphere composition

#### A1.3 Assembly Process - Plastic, Ceramic, or Flip-Chip BGA

#### a. Assembly Process

MEMS sensor overcoat (e.g., silicone gel)

#### A1.4 Qualification/Requalification Lot Requirements

Table A1.1: MEMS Part Qualification/Requalification Lot Requirements (see AEC-Q100 with additional requirements as shown below)

Part Information	Lot Requirements for Qualification
New MEMS design and no applicable generic data.	Lot and sample size requirements AEC-Q100 Table 2 and Tables 2A/2B of this specification.
Generic data available for the MEMS design, but in a different package.	Only MEMS device specific tests as defined in Section 4.2 are required. Lot and sample size requirements per AEC-Q100 Table 2 and Tables 2A/2B of this specification for the required tests.
Same MEMS design and package, but new circuit or IC (with similar geometry).	Review Table 3 (both AEC-Q100 and this specification) to determine which tests from AEC-Q100 Table 2 and Tables 2A/2B of this specification should be considered.
MEMS design change, MEMS fabrication process change, or package change.	Review Table 3 (both AEC-Q100 and this specification) to determine which tests from AEC-Q100 Table 2 and Tables 2A/2B of this specification should be considered.

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# Appendix 2: Q103-002 Certification of Design and Construction

Supplier Name:	Date:
Cappilo: Haillo:	Dato.

The following information is required to identify a device that has met the requirements of AEC-Q103-002. Submission of the required data in the format shown below is optional. **All entries must be completed; if a particular item does not apply, enter "Not Applicable".** This template can be downloaded from the AEC website at http://www.aecouncil.com.

#### This template is available as a stand-alone document.

Item Name	Supplier Response
1. User's Part Number:	
2. Supplier's Part Number/Data Sheet:	
3. Device Description:	
4.1. Control Wafer/Die Fab Location & Process ID:	
a. Facility name/plant #:	
b. Street address:	
c. Country:	
4.2. MEMS Wafer/Die Fab Location & Process ID:	
a. Facility name/plant #:	
b. Street address:	
c. Country:	
4.3. Cap Wafer/Die Fab Location & Process ID:	
<ul><li>a. Facility name/plant #:</li></ul>	
b. Street address:	
c. Country:	
4.4. Cap Wafer to MEMS Wafer Bonding	
Location & Process ID:	
a. Facility name/plant #:	
b. Street address:	
c. Country:	
5.1. Control Wafer Probe Location:	
a. Facility name/plant #:	
b. Street address:	
c. Country:	
5.2. MEMS Wafer Probe Location:	
<ul><li>a. Facility name/plant #:</li><li>b. Street address:</li></ul>	
c. Country:	
5.3. Bonded Wafer Probe Location:	
a. Facility name/plant #:	
b. Street address:	
c. Country:	
6. Assembly Location & Process ID:	
a. Facility name/plant #:	
b. Street address:	
c. Country:	

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7. Fir	nal Quality Control A (Test) Location:			
a.	Facility name/plant #:			
b.	Street address:			
C.	Country:			
	ontrol Wafer/Die:			
a.	Wafer size:			
b.	Die family:			
C.	Die mask set revision & name:			
d.	Die photo:	See attached	Not available	
8.2. MI	EMS Wafer/Die:			
a.	Wafer size:			
b.	Die family:			
C.	Die mask set revision & name:			
d.	Die photo:	See attached	Not available	
9.1. Co	ontrol Wafer/Die Technology Description:			
a.	Wafer/Die process technology:			
b.	Die channel length:			
C.	Die gate length:			
d.	Die supplier process ID (Mask #):			
e.	Number of transistors or gates:			
f.	Number of mask steps:			
	EMS Wafer/Die Technology Description:			
	Wafer/Die process technology:			
b.	Sensor length x width x depth:			
C.	Sensor anti-stiction coating:			
d.	Die supplier process ID (Mask #):			
e.	Number of sensor detection elements			
C.	(e.g., comb/fingers cells, pressure-			
	sensing cells, thermal cells, etc.):			
f.	Number of mask steps:			
	ap to MEMS Wafer Bonding Technology			
Descri				
a.	·			
b.	- · · · · · · · · · · · · · · · · · · ·			
D.	bonding:			
_				
C.	MEMS cavity pressure range after			
10.4	bonding:	Control Dia	MEMC Dia	Con Dia
10.1.	Die Dimensions:	Control Die	MEMS Die	Cap Die
a.	Die width:			
b.	Die length:			
C.	Die thickness (finished):			
d.	Membrane Thickness:			
10.2.	Capped MEMS Thickness:	Capped MEMS Wa	afer	
a.	After bonding:	Cappoa MEMO We	X101	
b.	Bonded wafer thinning process			
	escription:			
C.	Finished Capped MEMS die thickness:			
U.	i ilionea cappea memo de uliciness.			
11. Di	e Metallization:	Control Die	MEMS Die	Cap Die
a.	Die metallization material(s):			
b.	Number of layers:			
C.	Thickness (per layer):			
d.	% of alloys (if present):			

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12. Die	Passivation:	Control Die	MEMS D	ie	Cap Die
a.	Number of passivation layers:				
	Die passivation material(s):				
	Thickness(es) & tolerances:				
d.	MEMS Anti-stiction Coating:				
13.1. or Capp	Die Overcoat Material (e.g., Polyimide) bed MEMS Die (e.g., Gel):	Control Die		MEMS D	ie
	Cross-Section Photo/Drawing:	Control Die	MEMS D	ie	Cap Die
		See attached	See attac	ched 🗆	See attached
		Not available	Not avail		Not available
45 Dia	Dan Dankida				
	Prep Backside:	Control Die	MEMS D	ie	Cap Die
	Die prep method: Die metallization:				
	Thickness(es) & tolerances:				
	Separation Method:	Control Die	MEMS D	ie	Bonded MEMS
a.	Kerf width (μm):	00111101210			Die
b.	Kerf depth (if not 100% saw):				
C.	Saw method:		_	_	
		Single⊡ Dual⊡	Single _	Dual	Single⊡ Dual⊡
17. Die	Attach:	Control Die		MEMS D	Pie
	Die attach material ID:				
	Die attach method:				
C.	Die placement diagram:	See attached		See atta	
18. Pac	rkane:	Not available		Not avail	able 🔲
a.	Type of package (e.g., plastic, ceramic,				
	unpackaged):				
	Ball/lead count:				
C.	JEDEC designation (e.g., MS029,				
	MS034, etc.):				
d.	Lead (Pb) free (< 0.1% homogenous	Yes □ No			
e.	material): Package outline drawing:	Yes	⊔ Not av	/ailable	1
19.1.	Mold Compound:	occ attached	1101 41	ranabic _	
	Mold compound supplier & ID:				
	Mold compound type:				
C.	Flammability rating:	UL 94 V1 🔲	UL 94 V	0 🗌	
d.	Fire Retardant type/composition:				
	Tg (glass transition temperature)(°C):	CTE4 (balant Ta)		OTEO (ab.	a a Ta\
	CTE (above & below Tg)(ppm/°C):	CTE1 (below Tg) =			ove Tg) =
19.2.	Package Material Used Before or After	Supplier for items to coverage drawing v			vielvis material
a.	ver MEMS or Capped MEMS Die: Material type and ID:	coverage drawing \	with dimen	SIUI 15.	
b.	Minimum material coverage:	See a	attached [	□ No	ot available
Č.	Maximum material coverage:		attached [		nt available

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20.	1.	Die to Leadframe Wire Bond:		
	a.	Wire bond material:		
	b.	Wire bond diameter (mils):		
	C.	Type of wire bond at die:		
	d.	Type of wire bond at leadframe:	_	<u></u>
	e.	Wire bonding diagram:	See attached	Not available
20.	2.	Die to Die Wire Bond:		
	a.	Wire bond material:		
	b.	Wire bond diameter (mils):		
	C.	Type of wire bond at Control die:		
	d.	Type of wire bond at MEMS die:	_	
	e.	Wire bonding diagram:	See attached	Not available
21.	Lea	adframe (if applicable):	Control Die	MEMS Die
	a.	Paddle/flag material:		
	b.	Paddle/flag width (mils):		
	C.	Paddle/flag length (mils):		
	d.	Paddle/flag plating composition:		
	e.	Paddle/flag plating thickness (µinch):		
	f.	Leadframe material:		
	g.	Leadframe bonding plating composition:		
	h.	Leadframe bonding plating thickness		
		(μinch):		
	i.	External lead plating composition:		
	j.	External lead plating thickness (µinch):		
22.	Sul	bstrate (if applicable):		
	a.	Substrate material (e.g., FR5, BT, etc.):		
	b.	Substrate thickness (mm):		
	C.	Number of substrate metal layers:		
	d.	Plating composition of ball solderable		
		surface:		
	e.	Panel singulation method:		
	f.	Solder ball composition:		
	g.	Solder ball diameter (mils):		
23.	Un	packaged Die (if not packaged):		
		Under Bump Metallurgy (UBM)		
		composition:		
	b.	Thickness of UBM metal:		
	c.	Bump composition:		
	d.	Bump size:		
24.	He	ader Material (if applicable):		
		ermal Resistance:		
	a.	θ <sub>JA</sub> °C/W (approx):		
	b.	θ <sub>JC</sub> °C/W (approx):		
	C.	Special thermal dissipation construction		
	v.	techniques:		
26	Te	st circuits, bias levels, & operational		
_0.		nditions imposed during the supplier's life	See attached	Not available
		d environmental tests:		. Tot divalidatio

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27. Fault Grade Coverage (%)	% Not digital circuitry $\square$
28. Maximum Process Exposure Conditions:  a. MSL @ rated SnPb temperature: b. MSL @ rated Pb-free temperature: c. Maximum dwell time @ maximum process temperature:	* Note: Temperatures are as measured on the center of the plastic package body top surface.  at °C (SnPb) at °C (Pb-free)
Attachments:	Requirements:
Die Photo  Package Outline Drawing  Die Cross-Section Photo/Drawing	1. A separate Certification of Design, Construction & Qualification must be submitted for each P/N, wafer fab, and assembly location.
Wire Bonding Diagram	<ol> <li>Design, Construction &amp; Qualification shall be signed by the responsible individual at the supplier who can verify the above information is accurate and complete. Type name and sign below.</li> </ol>
Die Placement Diagram	
MEMS material coverage drawing with dimensions Test Circuits, Bias Levels, & Conditions	
Completed by: Date:	Certified by: Date:
Typed or Printed:	
Signature:	
Title:	

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# Appendix 3: Minimum Requirements for MEMS Pressure Sensor Qualification Plans and Results

The following information is required as a minimum to identify a device that has met the requirements of AEC-Q103-002 (see Appendix Templates <u>3A</u> and <u>3B</u>). Submission of data in this format is optional. However, if these templates are not used, the supplier must ensure that each item on the template is adequately addressed. The templates can be downloaded from the AEC website at http://www.aecouncil.com.

#### A3.1 Plans

- 1. Part Identification: Customer P/N and supplier P/N.
- 2. Site or sites at which life testing will be conducted.
- 3. List of tests to be performed (e.g., JEDEC method, Q100 and Q103-002 method, MIL-STD method) along with conditions. Include specific temperature(s), humidity, and bias to be used.
- 4. Sample size and number of lots required.
- 5. Time intervals for end-points (e.g., after PC, 0 hour, 500 hour, 1000 hour).
- 6. Targeted start and finish dates for all tests and end-points.
- 7. Supplier name and contact.
- 8. Submission date.
- 9. Material and functional details and test results of devices to be used as generic data for qualification. Include rationale for use of generic data.

#### A3.2 Results

All of above plus:

- 1. Date codes and lot codes of parts tested.
- 2. Process identification.
- 3. Fab and assembly locations.
- 4. Mask number or designation.
- 5. Number of failures and number of devices tested for each test.
- 6. Failure analyses for all failures and corrective action reports to be submitted with results.

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#### Appendix Template 3A: AEC-Q103-002 Qualification Test Plan

Appe		•		002 Qualification Test	Pian	1
	C	2103-002	2 QUALIFICATIO			
USER COMPANY:				DATE:		
USER P/N:				TRACKING NUMBER:		
USER SPEC #:				ER COMPONENT ENGINEER:		
SUPPLIER COMPANY:			SUPPLII	ER MANUFACTURING SITES:		
SUPPLIER P/N:			DE	PPAP SUBMISSION DATE:		
SUPPLIER FAMILY TYPE:				EASON FOR QUALIFICATION:	REQUIREMENTS	RESULTS
STRESS TEST	ABV	TEST#	TEST METHOD	Test Conditions/S.S. per Lot/# Lots (identify temp, RH, & bias)	S.S #LOTS	Fails/S.S./# lots
Preconditioning	PC	A1				
Temperature Humidity Bias or HAST	THB / HAST	A2				
Autoclave or Unbiased HAST	AC / UHST	A3				
Temperature Cycle	TC	A4				
Power Temperature Cycling	PTC	A5				
High Temperature Storage Life	HTSL	A6				
High Temperature Operating Life	HTOL	B1				
Early Life Failure Rate	ELFR	B2				
NVM Endurance, Data Retention, & Operational Life	EDR	В3				
Wire Bond Shear	WBS	C1				
Wire Bond Pull Strength	WBP	C2				
Solderability	SD	C3				
Physical Dimensions	PD	C4				
Solder Ball Shear	SBS	C5				
Lead Integrity	LI	C6				
Electromigration	EM	D1				
Time Dependent Dielectric Breakdown	TDDB	D2		450.0400		
Hot Carrier Injection	HCI	D3		AEC-Q100		
Negative Bias Temperature Instability	NBTI	D4				
Stress Migration	SM	D5				
Pre- and Post-Stress Electrical Test	TEST	E1				
ESD - Human Body Model	HBM	E2				
ESD - Charged Device Model	CDM	E3				
Latch-Up	LU	E4				
Electrical Distributions	ED	E5				
Fault Grading	FG	E6				
Characterization	CHAR	E7				
Electromagnetic Compatibility	EMC	E9				
Short Circuit Characterization	SC	E10				
Soft Error Rate	SER	E11				
Lead Free	LF	E12				
Process Average Test	PAT	F1				
Statistical Bin/Yield Analysis	SBA	F2				
Mechanical Shock	MS	G1				
Variable Frequency Vibration	VFV	G2				
Constant Acceleration	CA	G3				
Gross/Fine Leak	GFL	G4				
Package Drop	DROP	G5				
Lid Torque	LT	G6				
Die Shear Strength	DS	G7				
Internal Water Vapor	IWV	G8				

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Appendix Template 3A: AEC-Q103-002 Qualification Test Plan (continued)

STRESS TEST	ABV	TEST#	TEST METHOD	Test Conditions/S.S. per Lot/#			RESULTS
31KE33 1E31	ADV	ILSI#	TEST WIETHOD	Lots (identify temp, RH, & bias)	S.S	# LOTS	Fails/S.S./# lots
Pressure & High Temperature Operating Life Test	PrHTOL	PS1	JESD22-A108		77	3	
Pulsed Pressure Temperature Cycling	PPrTC	PS2	JESD22-A104		<u>45</u>	3	
Pressure & Low Temperature Storage Life Test	PrLT <u>S</u> L	PS3	JESD22-A119		77	1	
Testing in a saturated atmosphere in the presence of sulphur dioxide	CHS	PS4	DIN 50018		45	1	
Corrosive Atmosphere	CAtm	PS5	EN 60068-2-60 / Method 4		10	1	
Chemical Resistance	CR	PS6	ISO 16750-5		Var (5xChemical)	1	
Burst Pressure	BPr	PS7			15	3	
Proof Pressure	PPr	PS8			15	3	
Salt Immersion Test	SIT	PS9	MIL-STD-883 - 1002		15	1	
Dust	DST	PS10	MIL-STD-202G - 110A		15	1	
Internal Visual Inspection	IV	PS11	MIL-STD-883 - 2013		5	3	
Die Shear Test	DIS	PS12	MIL-STD-883 - 2019		5	3	
Supplier:		•		Approved by: (User Engineer)	•		

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# Appendix Template 3B: AEC-Q103-002 Generic Data

Objective:			Package:	-					Qual Plan Ref#:	:		
Device:			Fab/Assv/Test:						Date Prepared	:		
Cust PN: Maskset:			Device Engr: Product Engr:	-					Prepared by: Date Approved:			
Die Size:			Component Engr:						Approved by:	:		
				Τ				Differences		Differences		Differences
Test #	ABV	Q100 Test Conditions	End-Point Requirements	Sample Size/Lot	# of Lots	Total # Units	Part to be Qualified	from Q100/Q103	Generic Family part A	from Q100/Q103	Generic Family part B	from Q100/Q103
A1	PC											
A2	THB / HAST											
А3	AC / UHST											
A4	TC											
A5	PTC											
A6	HTSL											
B1	HTOL									<del> </del>		
B2	ELFR									<del></del>		
В3	EDR											
C1	WBS											
C2	WBP											
C3	SD											
C4	PD											
C5	SBS											
C6	LI											
D1	EM		AEC-Q100									
D2	TDDB											
D3	HCI											
D4	NBTI											
D5	SM											
E1	TEST											
E2	нвм											
E3	CDM											
E4	LU											
E5	ED											
<b>E</b> 6	FG											
E7	CHAR											
E9	EMC											
E10	SC											
E11 E12	SER LF											
F1	PAT											
F2	SBA											
G1	MS											
G2 G3	VFV CA											
G4	GFL											
G5	DROP											
G6	LT											
<b>G7</b>	DS											
G8	IWV											

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# Appendix Template 3B: AEC-Q103-002 Generic Data (continued)

Test #	ABV	Q100 Test Conditions	End-Point Requirements	Sample Size/Lot	# of Lots	Total # Units	Part to be Qualified	Differences from Q100/Q103	Generic Family part A	Differences from Q100/Q103	Generic Family part B	Differences from Q100/Q103
PS1	PrHTOL	JESD22- A108	TEST = ROOM, COLD, and HOT	77	3	231						
PS2	PPrTC	JESD22- A104	TEST = COLD and HOT	<u>45</u>	3	135						
PS3	PrLT <u>S</u> L	JESD22- A119	TEST = ROOM, HOT and COLD	77	1	77						
PS4	CHS	DIN 50018	TEST = ROOM	45	1	45						
PS5	CAtm	EN 60068-2-60 / Method 4	TEST = ROOM	10	1	10						
PS6	CR	ISO 16750-5	TEST = ROOM	Var (5/Chemical)	1	Var (5/Chemical)						
PS7	BPr		TEST = ROOM	15	3	45						
PS8	PPr		TEST = ROOM	15	3	45						
PS9	SIT	MIL-STD-883 - 1002	TEST = ROOM	15	1	15						
PS10	DST	MIL-STD-202G - 110A	TEST = ROOM	15	1	15						
PS11	IV	MIL-STD-883 - 2013		5	3	15						
PS12	DIS	MIL-STD-883 - 2019		5	3	15						

Part Attributes	Part to be Qualified	Generic Family Part A	Generic Family Part B
User Part Number			
Supplier Part Number			
	A1.1 Pr	oduct	
Product Functionality (e.g., Op- Amp, Regulator, Microprocessor, Logic – HC/TTL)			
Operating Supply Voltage Range(s)			
Specified MEMS Operating Temperature Range			
Specified MEMS Operating Frequency Range			
Specified MEMS Operating Pressure Range			
Specified MEMS Operating Mechanical Condition (e.g., general Pressure Sensor, rim or tire mounted TPMS)			
Specified MEMS Operating Environmental Condition (e.g., details of expected harsh operating environment)			
Analog Design Library Cells (e.g., active circuit elements, passive circuit elements)			
<sup>1</sup> Digital Design Library Cells (e.g., circuit blocks, IO modules, ESD cells)			
Memory IP (e.g., cell structure, building block)			
Memory Type(s) & Size(s)			
Design Rules for Active Circuits under Pads			
Other Functional Characteristics (as defined by supplier)			

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# Appendix Template 3B: AEC-Q103-002 Generic Data (continued)

Part Attributes	Part to be Qualified	Generic Family Part A	Generic Family Part B
	A1.2 Fab	Process	
Wafer Fab Technology (e.g.,			
CMOS, NMOS, Bipolar)			
Circuit Element Feature Size (e.g.,			
layout design rules, die shrinks,			
contacts, gates, isolations)			
Substrate (e.g., orientation, doping,			
epi, wafer size)			
Maximum Number of Masks			
(supplier must show justification for			
waiving this requirement)			
Lithographic Process (e.g., contact			
vs. projection, E-beam vs. X-ray,			
photoresist polarity)			
Doping Process (e.g., diffusion vs.			
ion implantation)			
Gate Structure, Material & Process			
(e.g., polysilicon, metal, salicide,			
wet vs. dry etch)			
Polysilicon Material, Thickness			
Range, & Number of Levels			
Oxidation Process & Thickness			
Range (e.g., gate & field oxides)			
Interlevel Dielectric Material &			
Thickness Range			
Metallization Material, Thickness			
Range, & Maximum Number of			
Levels			
Passivation Process (e.g.,			
passivation oxide opening),			
Material, & Thickness Range			
Die Backside Preparation Process			
& Metallization			
Wafer Fabrication Site			
MEMS Structure and Material			
MEMS Silicon Cap Bonding			
Process and Bonding Materials-			
MEMS Internal Atmosphere			
Composition			
Composition	A1.3 Assembly Proces	s – Plastic or Ceramic	
	711107100011121,7110000	I	
Assembly Site			
Package Type (e.g., DIP, SOIC,			
QFP, PGA, PBGA)			
Dance of Daddle/Flor Cine			
Range of Paddle/Flag Size			
(maximum & minimum dimensions)			
Qualified for the Die Size/Aspect			
Ratio Under Consideration			
Worst Case Package (e.g.,			
package warpage due to CTE			
mismatch)			
Substrate Base Material (e.g.,			
PBGA)			
Leadframe Base Material			
Die Header / Thermal Pad Material			
Leadframe Plating Material &			
Process (internal & external to the			
package)			
Die Attach Material			

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# Appendix Template <u>3B</u>: AEC-Q103-002 Generic Data (continued)

Part Attributes	Part to be Qualified	Generic Family Part A	Generic Family Part B
Wire Bond Material & Diameter			
Wire Bond Method, Presence of Downbonds, & Process			
Plastic Mold Compound Material, Organic Substrate Material, or Ceramic Package Material			
Plastic Mold Compound Supplier/ID			
Solder Ball Metallization System (if applicable)			
Heatsink Type, Material, & Dimensions			
Die Preparation/Singulation			
MEMS sensor Overcoat: Material or Process (e.g., silicone gel)			

Note 1: Design Library cells need to follow guidelines for temperature ranges, voltage ranges, speed, performance, and power dissipation as defined in Appendix 1 of this document.

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# **Revision History**

Rev#	Date of change	Brief summary listing affected sections
-	March 1, 2019	Initial Release
<u>A</u>	Sept. 17, 2023	Complete Revision. Revised section 1, 1.3.1, 1.3.3, 3.1, 3.2, and 4.2, Figures 1 and 2, Tables 2A, 2B and Table 3, Appendix Templates 2, 3A and 3B.